

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexadria, Vignia 22313-1450
www.uspu.gov

BIBDATASHEET

Bib Data Sheet

CONFIRMATION NO. 5440

| SERIAL NUMBER 10/633,576 | FILING DATE 08/05/2003 RULE | CLASS 257 438 | GROUP ART L 2811 | TINU | ATTORNEY CONTROL OF THE PROPERTY OF THE PROPER | |
|---|------------------------------|---------------------|---|--|--|--|
| APPLICANTS | | | | | | |
| Hiroshi Maeda, Hyogo, JAPAN; | | | | | | |
| Toshiyuki Oashi, Hyogo, JAPAN; Takashi Uehara, Osaka, JAPAN; | | | | | | |
| ** CONTINUING DATA ********************************** | | | | | | |
| YES TT | | | | | | |
| ** FOREIGN APPLICATIONS ************************************ | | | | | | |
| YES TY | | | | | | |
| F REQUIRED, FOREIGN FILING LICENSE GRANTED ** 10/30/2003 | | | | | | |
| Foreign Priority claimed 35 USC 119 (a-d) conditi | ons yes no Met after | STATE OR Allowance | SHEETS | TOTAL | INDEPENDENT | |
| Verified and Acknowledged | IVIANN KINN | TT COUNTRY JAPAN | DRAWING 9 | CLAIMS 5 | CLAIMS 1 | |
| ADDRESS McDermott, Will & Emery 600 13th Street, N.W. Washington , DC 20005-3096 | | | | | | |
| TITLE Method of manufacturing a semiconductor device with capacitor electudes Semiconductor device with dapacitor electrodes and method of manufacturing thereof | | | | | | |
| | | | ************************ | ☐ All Fees | | |
| | | | *************************************** | ☐ 1.16 Fees (Filing) ☐ 1.17 Fees (Processing Ext. of | | |
| Noto charge/credit DEPOSIT ACCOUNTtime) | | | | | | |
| RECEIVED No. | No for following: | | | ☐ 1.18 Fees (Issue) | | |
| | | | | □ Other □ Credit | | |
| | | | | | | |